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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE **Group 2700**

Inventor: KISMAL, Christopher and)
WILSTRUP, Jan)
Serial No: 09/039,344)
Filing Date: March 13, 1998)
Title: Time Interval Measurement System)
Incorporating a Linear Ramp)
Generation Circuit)

PATENT

Art Group Unit: 2764

Examiner: Not Assigned

Atty. Docket: 16,468-08

Honorable Commissioner of Patents and Trademarks
c/o ASSISTANT COMMISSIONER OF PATENTS
Washington, D.C. 20231

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INFORMATION DISCLOSURE STATEMENT UNDER 1.56 DEC 01 1998

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Applicants herein make available to the Patent and Trademark Office a copy of PTO-1449 which lists pertinent art related to the invention described in the above-captioned application. Copies of the documents are enclosed. This Information Disclosure Statement is being filed in accordance with the following provisions:

- [x] 37 CFR § 1.97(b)(3) Before the mailing date of a first Office Action on the merits. No fee is required.

The examiner is requested to consider carefully the complete text of these documents in connection with the above-identified application in accordance with 37 CFR 1.104(a). It is requested that the documents listed on the attached form PTO-1449 be included in the "References Cited" portion of any patent issuing from this application (M.P.E.P. 1302.12), and that the Examiner initial and return a copy of the form to evidence consideration of the documents.

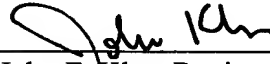
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Respectfully submitted,

Date: Oct. 15, 1998



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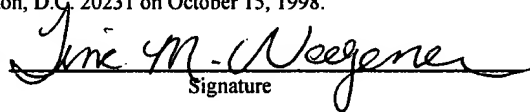
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"CERTIFICATE OF MAILING UNDER 37 CFR 1.8: I hereby certify that this paper and any papers referred to herein are being deposited with the U.S. Postal Service, as first class mail, postage prepaid, addressed to the Commissioner of Patents and Trademarks, c/o Assistant Commissioner of Patents, Washington, D.C. 20231 on October 15, 1998.

Tina W. Wagener


Signature

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